

Tuesday (11/2/10)

7:00 am	Track 1 – ReSpace and Track 2 – MAPLD Plenary Session	Breakfast
8:00 am – 8:15 am		Opening Remarks: Steve Suddarth - COSMIAC
8:15 am – 8:45 am		History Of Survey: Brent Abbot - Surrey
8:45 am – 9:15 am		History Of Cube Sats: Jordi Puig-Suari - Calpoly
9:15 am – 9:45 am		Small Sats, Electronics and “Wicked” Problems: Steve Nixon - Consultant
9:45 am – 10:15 am		Break
10:15 am – 10:30 am		NASA Opening Remarks: Wes Powell – NASA/Goddard
10:30 am – 11:00 am		Trends In Reconfigurable And High Performance Electronics For Space: Ian Troxel - SEAKR
11:00 am – 11:30 am		Space Plug-and-play Avionics (SPA): James Lyke - AFRL
11:30 am – 12:15 pm		Break
12:15 pm		Lunch and Panel Session (Are You Crazy Flying That?) – Panel Moderator: Ken LaBel - NASA

Tuesday (11/2/10) contd...

1:30 pm – 2:00 pm	Track 1 – ReSpace Session I - SPA Standardization	Kit Of Parts Architecture: Howe
2:00 pm – 2:30 pm		GNC Hierarchy Using SPA: Jane Hansen - HRP Systems, Inc. / Paul Graven - Microcosm, Inc.
2:30 pm – 3:00 pm		SPA Network Management: Jacob Christensen - Utah State University / Scott Cannon, Bryan Hansen - Space Dynamics Laboratory
3:00 pm – 3:30 pm		Break
3:30 pm – 4:00 pm		Advanced Plug - n - Play Technologies (APT) Lessons Learned: Steve Schenk, Maureen Huston, Stanley O. Kennedy Jr. – Comtech AeroAstro, Inc.
4:00 pm – 5:30 pm		Making SPA Work: Industry Input, The Standards Process, And Where We Are Now – Panel Chair: Millay Morgan, APT
6:15 pm		Dinner – “Meet The Experts”

Tuesday (11/2/10) contd...

1:30 pm – 1:40 pm	Track 2 – MAPLD Session A – Mil / Aero Market Update For FPGAs And Associated Devices	Non-Volatile And Volatile Carbon Nanotube Electronics For Reconfigurable, Radiation Hard Space Based Applications' For The 2010 ReSpace/MAPLD Conference: Jonathan W. Ward, Adrian Robinson, Garo Derderian, Michael O'Connor, Quoc Ngo, Scott Anderson, Daniel Etter, James Dunning, E. James Egerton, Gerry Taylor - Lockheed Martin Nanosystems
1:40 pm – 1:50 pm		Very High Density And Radiation Tolerant PROM For Space Systems: Pierre Xiao WANG, Pierre-Eric Berthet - 3D PLUS 641 Rue Hélène Boucher 78532 BUC Cedex, France
1:50 pm – 2:00 pm		90nm Design-Hardened Structured ASIC, The Next Generation Of Digital ICs For Space: Stephen Philpy - Micro-RDC
2:00 pm – 2:10 pm		Actel's RTAX4000D Space Qualification Update: Solomon Wolday, Marco Cheung, Antony Wilson - Actel Corporation
2:10 pm – 2:20 pm		Space Qualified Ultra-High Speed Plug-and-Play Radiation Hard SERDES: Vladimir Katzman, Vladimir Bratov, Armando Otero, Jeb Binkley - ADSANTEC
2:20 pm – 2:30 pm		Aeroflex Non-Volatile Boot Memory For Xilinx FPGAs: Ron Lake - Aeroflex Colorado Springs
2:30 pm – 2:40 pm		Radiation Hardened Integrated Point-of-Load DC-DC Converter on Sapphire: J. W. Swonger, G. Wu, C. Olson, D. Robinette - Peregrine Semiconductor
2:40 pm – 2:50 pm		High Speed Radiation Hard 72Mbit Quad Data Rate SRAM For Space Applications: Helmut Puchner, Brian McMinn, Walt Anderson, Jake Tausch - Cypress Semiconductor, JD Instruments
2:50 pm – 3:00 pm		Introducing The Virtex-5QV Reconfigurable FPGA: The Biggest And Fastest Space-grade Part On The Planet: Carl Carmichael, Gary Swift, Delfin Rodillas
3:00 pm – 3:30 pm		Break

Tuesday (11/2/10) contd...

3:30 pm – 3:40 pm	Track 2 – MAPLD Session A – Mil / Aero Market Update For FPGAs And Associated Devices (contd...)	JPIC - Rad-Hard JPEG2000 Image Compression ASIC: Ran Ginosar, Dov Alon, Nikos Zervas, Amitai Broyde - Ramon Chips Ltd., Alma Technologies, S.A., MBT Ltd.
3:40 pm – 3:50 pm		ATMEL ATF280F Rad Hard SRAM Based Dynamic Partial Reconfiguration: Bernard Bancelin – Atmel ASIC Business Unit
3:50 pm – 4:00 pm		The Advantages And Challenges Of Using Many-core Processors For Space-based Applications: James Jones, Michael Doerr - Coherent Logix
4:00 pm – 4:10 pm		RadRunner: Daniel Elftmann - Achronix Semiconductor

4:10 pm – 4:30 pm	Track 2 – MAPLD Session B – Flight / Field Plans And Experiences With FPGAs And PLDs	LIDAR Image processing acceleration via FPGA implementation: Dr. Tak-kwong Ng, Mr. Carl Mills, Dr. Alex Bulyshev, Dr. Robert Hodson
4:30 pm – 4:50 pm		SpaceCube On-Board Science Data Processor: Current Applications and Future Architectures: Dan Espinosa – NASA / GSFC
4:50 pm – 5:10 pm		LANL CubeSat Reconfigurable Computer (CRC): Keith S. Morgan, Lauren Ho, Zachary A. Jacobs - ISR-3 Space Data Systems, Los Alamos National Laboratory
5:10 pm – 5:30 pm		Highly Scalable/Highly Configurable FPGA Based Solid State Recorder for Multi-Mission Space Applications: Charlie Howard - Southwest Research Institute
6:15 pm		Dinner – “Meet The Experts”