

Wednesday (11/3/10)

7:30 am	Track 1 – ReSpace and Track 2 – MAPLD Session II - Combined Session – Software Defined Radio	Breakfast
8:30 am – 8:50 am		Development Of NASA’s Space Communications And Navigation Test Bed aboard ISS To Investigate SDR, On-board Networking And Navigation Technologies – Richard C. Reinhart – NASA/Glenn Research Center
8:50 am – 9:10 am		The Firehose Adaptive Software Defined Radio – Joseph M. Palmer, Michael P. Caffrey, Keith S. Morgan - Adaptive Radio Technologies, LLC
9:10 am – 9:30 am		Software Defined Payloads – Alan Mast - Harris Corporation
9:30 am – 10:00 am		GENSO, CubeSats and Space Dial Tone – Craig Kief - Configurable Space Microsystems Innovations & Applications Center (COSMIAC) / Naveen N. Purushotham - Univ. of New Mexico / R. Scott Erwin, Joshua F. Androlewicz and James Lyke - AFRL, Space Vehicle Directorate / Julius R. Jackson - Central State University
10:00 am – 10:30 am		Break
10:30 am – 10:50 am		Research And Development Of A Flexible Communication Platform For Space – Christopher Sauer - Southwest Research Institute
10:50 am – 11:10 am		Responsive Satellite Communications Via FPGA-Based Software-Defined Radio For SPA-U Compatible Platforms – Alexander M. Wyglinski - University of New Mexico (UNM) / L. Howard Pollard, Jim Aarestad, Craig Kief - Configurable Space Microsystems Innovations & Applications Center (COSMIAC)
11:10 am – 11:30 am		Software Defined Radio – Kevin J. Lynaugh - Vulcan Wireless Inc.
11:30 am – 12:15 pm		Break
12:15 pm		Lunch

Wednesday (11/3/10) contd...

1:30 pm – 2:00 pm	Track 1 – ReSpace Session III - New Architectures For Aero Space	Turning Vision Into Reality: Phidget
2:00 pm – 2:30 pm		Integration Of The Self-Healing eDNA Architecture In An Embedded System And Evaluation Of A Fourier Transform Spectrometer Instrument Application – Michael Reibel Boesen, Didier Keymeulen, Jan Madsen, Thomas Lu - Technical University of Denmark
2:30 pm – 3:00 pm		AFRL’s AI & T Tool Flow Software: Eddy
3:00 pm – 3:30 pm		Next Generation Plug n' Play Radiation Tolerant Avionics: Anthony Lai - Aitech Defense Systems
3:30 pm – 4:00 pm		Break
4:00 pm – 4:30 pm		On-demand On-line Scrubbing Of The RHBD Structured ASIC Appliqué Sensor Interface Module – Sasan Ardalan, Michael Sibley, Alonzo Vera, Don Elkinis - Microelectronics Research Development Corporation
4:30 pm – 5:00 pm		Spacecraft Plug And Play Avionics Experiment - On Orbit Results: Maurice Martin – AFRL/RVSE
5:00 pm – 5:30 pm		Introducing A Low Cost And High Performing Interoperable Satellite Platform Based On Plug-and-Play Technology For Modular And Reconfigurable Civilian And Military Nanosatellites: Fredik C. Bruhn – AAC Microtec AB / Perselin – University Of Applied Sciences / Robert Lindegren – Bremen / Indulis Dalnins – Air Force Research Laboratory / James Lyke, Josette Rosengren Calxite, Rickard Nordenberg – Swedish Defence Material Administration (FMV)
5:30 pm – 6:00 pm		LV SPA: Pat McGuirk – PnP Innovations
6:30 pm		Dinner – Speaker: Hon. Heather Wilson, Former U.S. Representative, NM 1st Congressional District

Wednesday (11/3/10) contd...

1:30 pm – 1:50 pm	Track 2 – MAPLD	Field Programmable Gate Array (FPGA)-Based Digital Signal Processor (DSP) for the NASA Soil Moisture Active Passive (SMAP) Radio-frequency Interference (RFI) Mitigating Radiometer: Damon Bradley, Cliff Brambora, Rafael Garcia, Lynn Miles, Mark Englin Wong, Jeff Piepmier - NASA/GSFC
1:50 pm – 2:10 pm	Session B – Flight / Field Plans and Experiences With FPGAs And PLDs (contd...)	HXiS-The HyperX Processor In Space: James Jones, Michael Doerr, Michael Solka - Coherent Logix
2:10 pm – 2:30 pm		Flexible Space Computing Architectures and the Role of FPGAs: Jeff Kalb – Sandia National Laboratories

Wednesday (11/3/10) contd...

2:30 pm – 2:50 pm	Track 2 – MAPLD Session C – Verification And Validation Of FPGAs and PLDs	Impact Of Ion-Induced Meta-Stable Conditions On Clocked Operations Of DICE Flip-Flops for Reconfigurable Devices: Dolores A. Black, Robert A. Reed, William H. Robinson, Jeffrey D. Black, Daniel B. Limbrick, Kevin D. Dick - IEEE
2:50 pm – 3:10 pm		Fault Injection Results Of Linux On An FPGA Embedded Platform: Joshua Monson, Dr. Brad Hutchings, Dr. Mike Wirthlin - Brigham Young University
3:10 pm – 3:30 pm		Bit-Accurate, At-Speed Hardware Verification For DO-254 Compliance: - ALDEC, Inc.
3:30 pm – 4:00 pm		Break
4:00 pm – 4:20 pm		A comparison Of The effectiveness Of SEE mitigation On RTAX-S/SL And ProASIC3E FPGAs: Alex McGuffey - Valparaiso University
4:20 pm – 4:40 pm		R3TOS - Reliable Scheduling and Allocation of Real-time Hardware Tasks onto Partially Damaged Xilinx FPGAs: Xabier Iturbe, Mikel Azkarate, Imanol Martinez, Antonio Perez - IKERLAN-IK4 Research Alliance / Khaled Benkrid, Ahmet T. Erdogan, Tughrul Arslan - The University of Edinburgh
4:40 pm – 5:00 pm		The Challenge Of Hold Time Check In Timing Critical FPGA Designs: Nizar Abdallah, Denis Hommais, Amal Zerrouki - Actel Corporation
5:00 pm – 5:20 pm		Framework For Independent Validation And Verification Of Complex Space FPGAs For NASA Spacecraft: Marco Figueiredo, N. Christopher Devine - MEI Technologies, Inc.
6:30 pm		Dinner – Speaker: Hon. Heather Wilson, Former U.S. Representative, NM 1st Congressional District